Meeting Notes January 23, 2014

Attendees: Stephen Kaye, Pravin Chordia, Mahesh Burse

**Minutes**

* Discussed the delivery of the clock driver card and backplane. Some customs broker information was requested and provided. Looks like all paperwork is in place.
* IUCAA wants access to clock driver test computer to set up software. We will have to check on the sys admin policy on opening up computers to others outside Caltech.
* Mahesh added that software and documentation will be ready by next week. We can try installing and compiling the code. We will have to wait for the full test if the hardware hasn’t arrived.
* The old waveform generation document can be reviewed for familiarity of programming waveforms, but an updated document will be provided.
* The backplane connectors need to be defined. Pravin will identify the outputs of the single board controller, and Steve will identify the signals for the cables in the system. A backplane mapping will be made from this information.
* Pspice simulations will have to wait until spice libraries are obtained for some parts in the design. This holds up some of the final values in the SBC schematic.
* Design document template was received and will be reviewed. Any questions and comments will be directed at Caltech, and at the same time, the document will be written.

**Action Items**

**IUCAA**

* + Send pinouts for the single board controller board.
  + Post documents on twiki concerning the clock driver board delivery
  + Post software on the twiki which will be used for the clock driver board.
  + Continue spice simulations for SBC
  + Begin writing the design document for the clock drivers

**Caltech**

* + Send pinouts for the cables and connector information.
  + Check with Caltech system administrators concerning allowing access to on-campus computers.