Meeting Notes October 24, 2013

Attendees: Stephen Kaye, Pravin Chordia, Mahesh Burse

**Minutes**

* Pravin presented and discussed the most recent schematics.
* Filters were added for the offset voltages in the signal chain
* Component values are still incomplete. Analysis of the circuit with the chosen component values is desired. This is the only way to know if the circuit will meet specifications.
* DAC output ranges were changed for the bias voltages.
* The filters and DAC voltage range were the only changes to the schematic.
* The computer requirements for the prototype demo were presented. The upshot being the computer needs to be a 64 bit linux machine.
* The power requirements were presented too. The power draw seems high, but they will be used for the demo.
* Schematics and documentation will be sent along with the demo package.
* Different waveform modes were discussed.
* Waveforms can be called from the host computer, but there is some latency.
* There is some confusion as to whether there are multiple waveform tables or just a single one. The present conclusion is there are multiple waveform tables.
* Multiple parameter files can be sent during operation, so clocking directions can be changed flexibly.
* Completed waveforms for our desired modes need to be completed to send for clarification.

**Action Items**

**IUCAA**

* + Analyze offset voltages for the signal chain. Choose values based on calculations, reasoning, and desired performance.
  + Continue getting the prototype and spare system ready.
  + Download and test waveforms sent by Caltech

**Caltech**

* + Assist in analyzing circuits for the signal chain.
  + Look over DAC datasheet for circuit analysis
  + Complete waveform tables and send along to IUCAA