Meeting Notes October 10, 2013

Attendees: Stephen Kaye, Pravin Chordia

**Minutes**

* Pravin presented and discussed results from clock driver testing. The tests involved the LM7171 and the new LM6171 opamps. The results can be found on the twiki.
* Simulations of the clock driver circuit shows good transient response with not overshoot/undershoot. The simulation, though, had the amplifier in inverting configuration.
* Simulations will be redone with a non-inverting configuration.
* A higher input voltage will be applied to the simulation as well
* Readiness for the prototype demo in October was discussed. A computer and power supply are needed at Caltech for the test
* Information concerning power supply capacity for the test will be sent
* Information concerning operating system for the computer to run the prototype will be sent.

**Action Items**

**IUCAA**

* + Do SPICE simulation of clock driver circuit
	+ Review latest controller definition document

 **Caltech**

* + Continue analysis of clock driver circuit
	+ Email comments on waveform generation document