Meeting Notes July 18, 2013

Attendees: Stephen Kaye, Roger Smith, Pravin Chordia, Mahesh Burse

**Minutes**

* Discussed phased delivery schedule. IUCAA posted a list of questions about the controller description
* Caltech needs to develop full controller description to serve as a specification.
* Discussed the questions about the preliminary DCDS schematic. Many of the questions were answered in a document posted to the twiki
* Power and area calculation without the LM6321 were posted and discussed
* Steve to indicate number of clocks and biases etc. to be used in area and power calculator
* Pravin will combine area and power calculators into common spreadsheet with one set of specs for resource counts.
* Concentrate on finalizing analog schematics and then decide whether present version is sufficiently similar to test at Caltech
* Mahesh will ship current version of clock driver in October. Working on testing now.

**Action Items**

**IUCAA**

* + Continue testing clock boards and building second set
  + Combine area and power calculations into common spreadsheet with on set of specs for resource count

**Caltech**

* + Review clock and bias ganging and determine the number of resources each needs
  + Develop full controller description in answer to IUCAA’s questions