**Power Supply Currents Measurement For Clock Driver Card.**

1. +5V     :  400 mA

Used to Power Digital Section i.e. FPGA. Load is close to quiescent as only firmware running is 15 clocks generated by FPGA.

1. +15V /- 15 V ~ 400 mA each i.e. total 800 mA into GND

This power is used for Gain Amplifier and Buffer i.e. Driver Amplifier. All the 30 Channels are powered and 15 Clocks running without Capacitive Load. This is close to quiescent current.

1. +/- 5.5V ~ 375 mA each i.e. total 750 mA in GND.

All DACs runs on this power supply. All DACs are powered and half of them are generating DAC voltages driving gain stage.