

Surface counts for the five NGAO architectures.  
Version 0.8

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# 1 Introduction

The NGAO systems engineering team came up with five architectures during the course of meeting #8. This document describes the architectures using schematics that were made during the meeting and tabulates surface count to science instrument, LGS WFSs and d-NIRI.

## 2 NGAO Architectures

The architecture suggested were:

1. SplitRelay - Split 20" Narrow field instruments / 120" d-NIRI relay architecture (see figure 1)
2. AM2 - Adaptive secondary mirror architecture (see figure 2)
3. LargeRelay - Single 180" TFOV relay architecture (see figure 3)
4. KI Upgrades - Keck I upgrade path architecture (see figure 4)
5. CascadedRelay - A variant of LargeRelay in which a 2nd stage is used for narrow-field instruments, reducing size (see figure 5)

### 2.1 Assumptions

In order to make an *apples-to-apples* comparison of the five architectures the following assumptions are made:

1. d-NIRI has the TT sensors packaged into it. Which makes all the narrow field instruments look through its dichroic pick off. Alternately one could envision a separate TT sensor package dedicated to the narrow field instruments (this is not considered in this document).
2. DMs are on TT stages where ever necessary, no extra surfaces are used for TT except in case of large DMs (K1 upgrade and Large Relay). A second TT stage is assumed in case of large relays to equalize the TT bandwidth between the architectures. d-NIRI has its own TT stage (MEMS DMs can be mounted on this TT stage if we use MEMS DM's) to allow for dithering. It is assumed that buying more stoke on the DM to use its surface for TT correction is more expensive than using a stage. It is assumed that the Adaptive Secondary has enough TT bandwidth.
3. All AO relays are reflective, the MEMS DM has a sapphire window on it and hence contributes to 5 surfaces when light bounces off of it.
4. A Risley prism pair based ADC design is assumed. For all other architectures the ADC is used only for the narrow field science path.

5. There are two enclosure windows to prevent condensation on the cold AO system in all cases except the ASM. The ASM option has the least number of surfaces and hence may need to get cooled lesser and so may be able to achieve performance with just one window.
6. In case of cascaded relay, the K mirror is in front of the large relay for convenience. So both LGS WFSs and d-NIRI are stationary.
7. PSF camera pick offs are not considered for surface count and it is envisioned that acquisition camera pick-off moves out of the way during science observations.
8. I have included the original meeting 8 schematics along with ones generated after the retreat. Color coding is used on the new schematics to represent different features. Optics/ subsystems shown in green rotate, MEMS DMs are magenta in color while piezo DMs are blue colored. d-NIRI/ TT pick off and path are shown in red and yellow coloration is used for the LGS WFS pick off, path and enclosure.

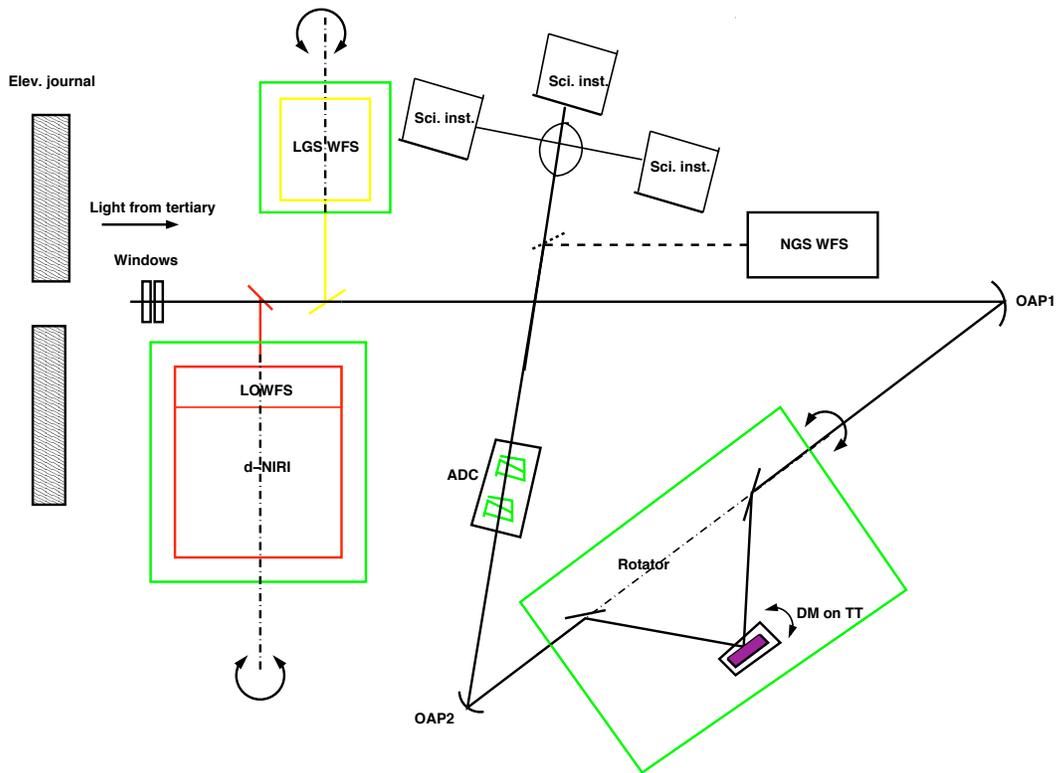


Figure 1: Functional schematic of split relay (30" Narrow field instruments / 120" d-NIRI relay architecture)

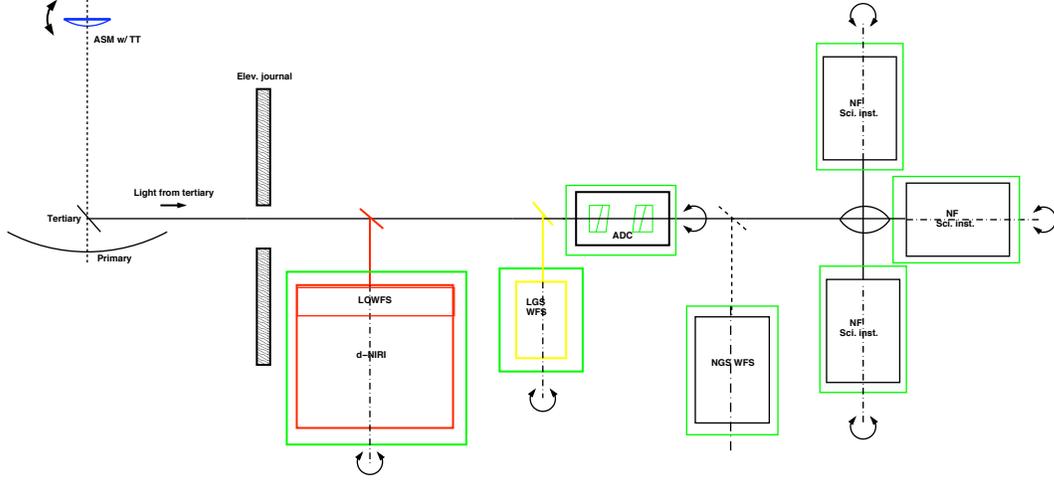


Figure 2: Functional schematic of adaptive secondary mirror architecture

Arch.	Tel.	N.F. AO	W.F. AO	K mirror	Na-dichroic	d-NIRI pickoff	ADC	2 <sup>nd</sup> TT	Entrance window(s)	Sci. fold	Total
Split Relay	3	3+4 <sup>⊕</sup>	-	2*	2	2	6	-	4	1	27
ASM	3	-	-	- <sup>◦</sup>	2	2	6	-	-	1	14
Large Relay	3	-	3+1 <sup>†</sup>	3	2	2	6	1	4	1	26
K1 upgrade	3	3	4 <sup>‡</sup>	3	2	2	6	1	4	1	29
Cascaded relay	3	3+4 <sup>⊕</sup>	3	3	2	2	6	-	4	1	31

Table 1: Table of surface count to the narrow field Science instrument for different NGAO architectures; \*- DM is already counted as part of NF AO relay, <sup>†</sup> - MCAO option, <sup>‡</sup> - extra fold mirror due to packaging constraint, <sup>◦</sup> - Science instrument rotates, <sup>⊕</sup> - 4 more surfaces as the MEMS DM is hermitically sealed using a window.

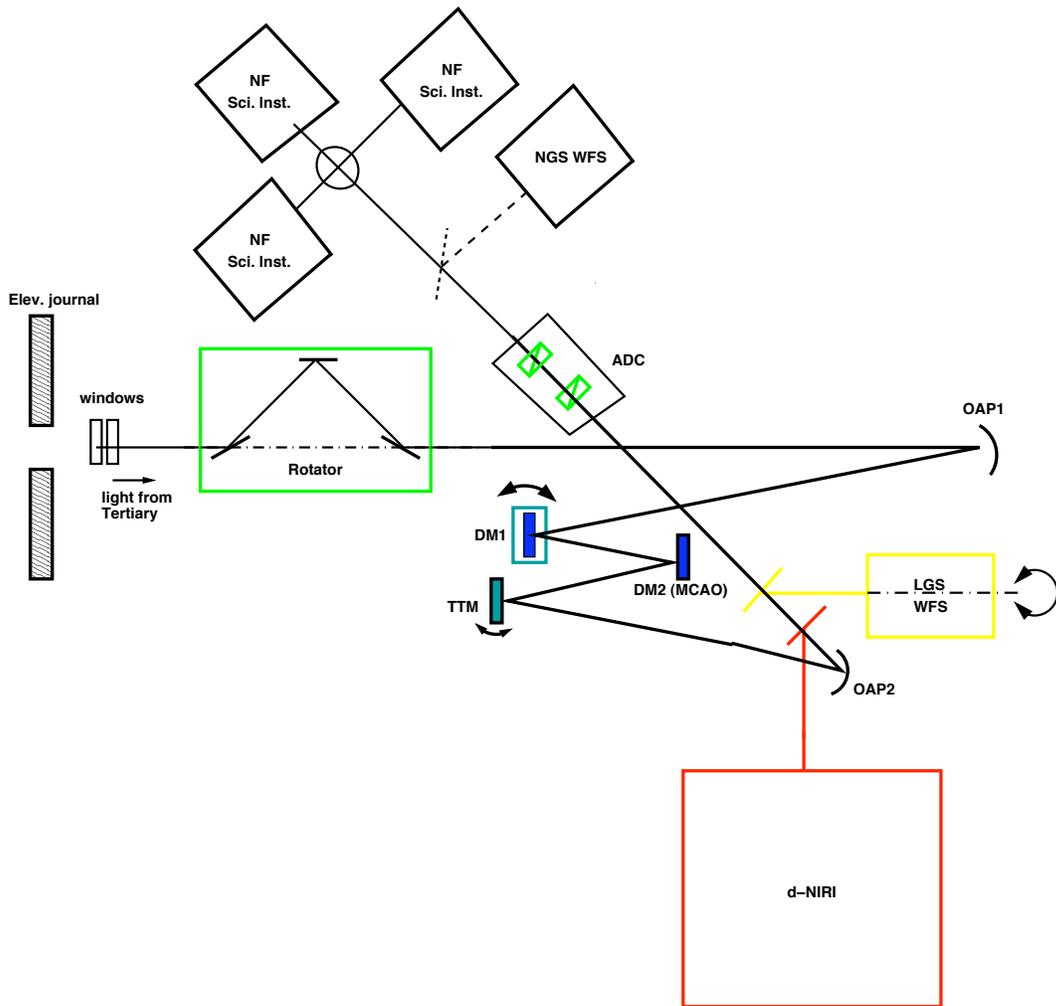


Figure 3: Functional schematic of large relay (Single 180'' TFoV relay architecture)

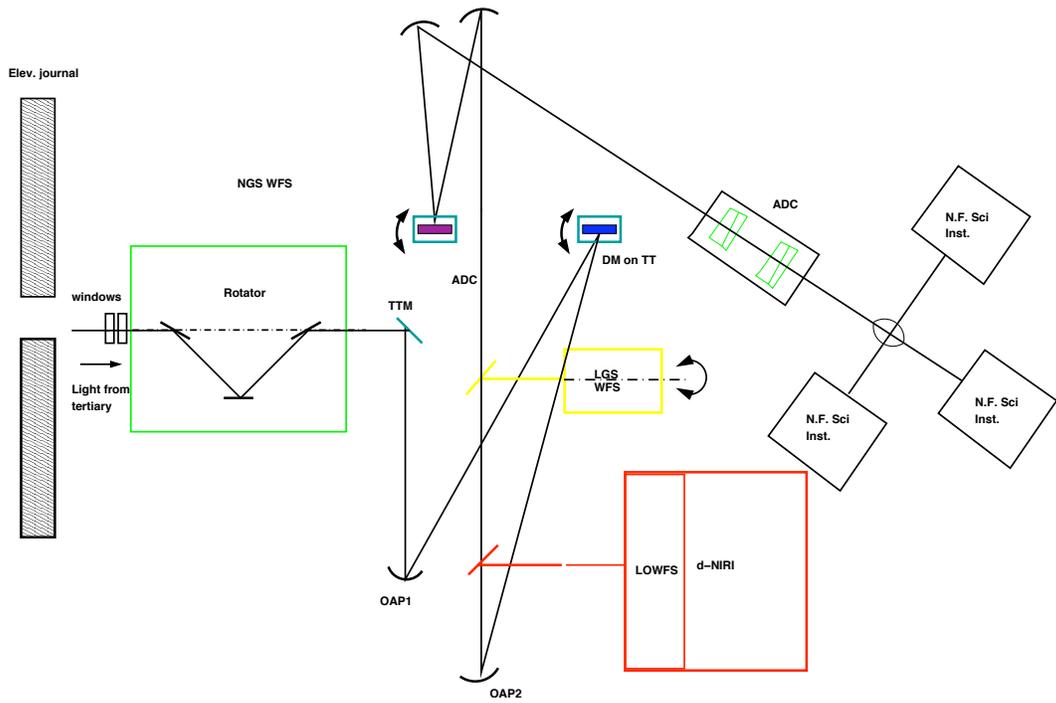


Figure 4: Functional schematic of Keck I Upgrade path architecture

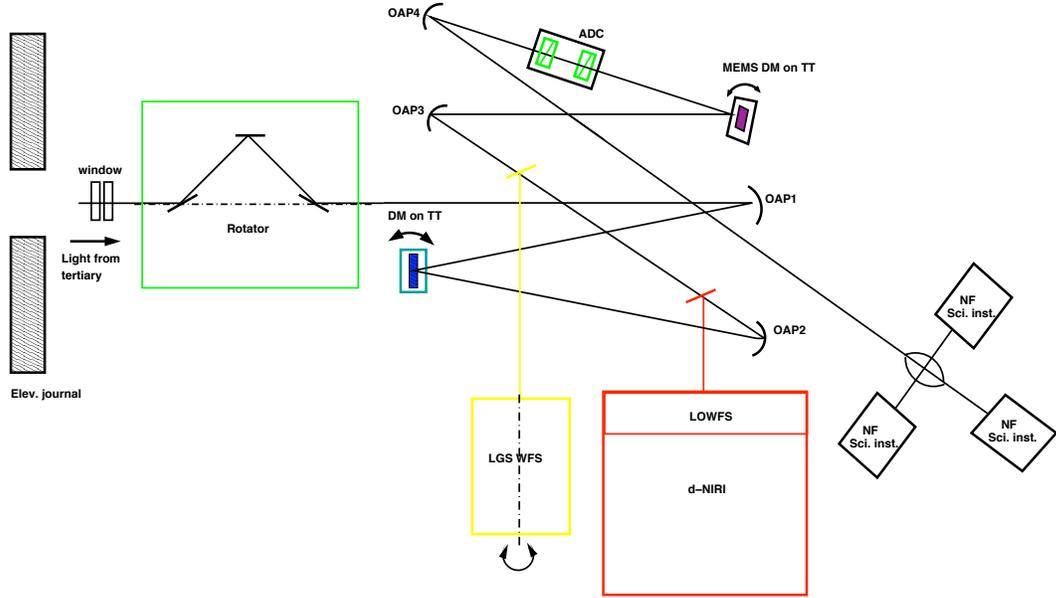


Figure 5: Cascaded relay architecture - as envisioned now

Arch.	Tel.	N.F. AO	W.F. AO	K mirror	Na-dichroic	d-NIRI pickoff	ADC	2 <sup>nd</sup> TT	Entrance window(s)	Total
Split Relay	3	-	-	- <sup>o</sup>	1	2	-	-	2 <sup>††</sup>	8
ASM	3	-	-	- <sup>o</sup>	1	2	-	-	-	6
Large Relay	3	-	3+1 <sup>†</sup>	3	1	-	-	1	4	16
K1 upgrade	3	-	4 <sup>‡</sup>	3	1	2	6	1	4	24
Cascaded relay	3	-	3	3	1	2	-	-	4	16

Table 2: Table of surface count to the LGS WFSs for different NGAO architectures; <sup>††</sup> - one window before the LGS WFSs and another before it goes into the AO enclosure, -<sup>o</sup> - LGS WFSs rotate.

Arch.	Tel.	N.F. AO	W.F. AO	K mirror	Na-dichroic	d-NIRI pickoff	ADC	2 <sup>nd</sup> TT	Entrance window(s)	Total
Split Relay	3	-	-	- <sup>o</sup>	-	1	-	-	4	8
ASM	3	-	-	- <sup>o</sup>	-	1	-	-	0	4
Large Relay	3	-	3+1 <sup>†</sup>	3	-	1	-	1	4	16
K1 upgrade	3	-	4 <sup>‡</sup>	3	2	1	6	1	4	24
Cascaded relay	3	-	3	3	0	1	-	-	4	14

Table 3: Table of surface count to d-NIRI for different NGAO architectures, -<sup>o</sup> - d-NIRI rotates.